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PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM 10079337	FILING DATE 02/20/2002	CLASS 716 713	SUBCLASS 13	GAU 2825 2833	EXAMINER <i>Dingon</i>
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**APPLICANTS: Fisher Rory;

**CONTINUING DATA VERIFIED:

no

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** FOREIGN APPLICATIONS VERIFIED:

no

PG-PUB	DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>
Foreign priority claimed <input type="checkbox"/> yes <input checked="" type="checkbox"/> no		ATTORNEY DOCKET NO
35 USC 119 conditions met <input type="checkbox"/> yes <input checked="" type="checkbox"/> no		10011093-1
Verified and Acknowledged Examiners's initials <i>MYA</i>		
TITLE : System and method for generating integrated circuit boundary register description data		
U.S. DEPT. OF COMM./PAT & TM-PTO-435L (Rev. 12-94)		

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
Assistance		Total Claims	Print Claim for O.G.
ISSUE FEE		DRAWING	
Amount Due	Date Paid	Sheet Drwg.	Figs. Drwg.
TERMINAL		Print Fig.	
DISCLAIMER		Application Examiner	
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